

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: Not Applicable	Designed for: Public Release	Mod. Date: 11/16/2022
TID #: TIDA-010236	Project Title: TIDA-010236 Main Board	
Number: TBD	Rev: B	Sheet Title: Block diagram
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 1 of 6
Drawn By: Hely Zhang	File: Cover page.SchDoc	Size: A4
Engineer: Hely Zhang	Contact: http://www.ti.com/support	

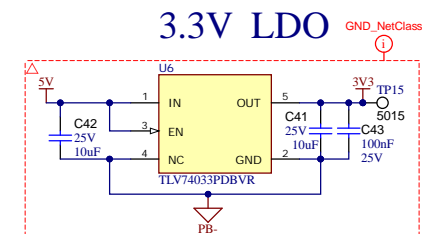
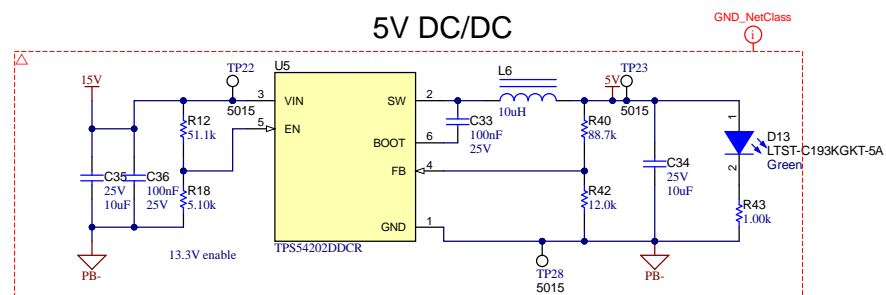
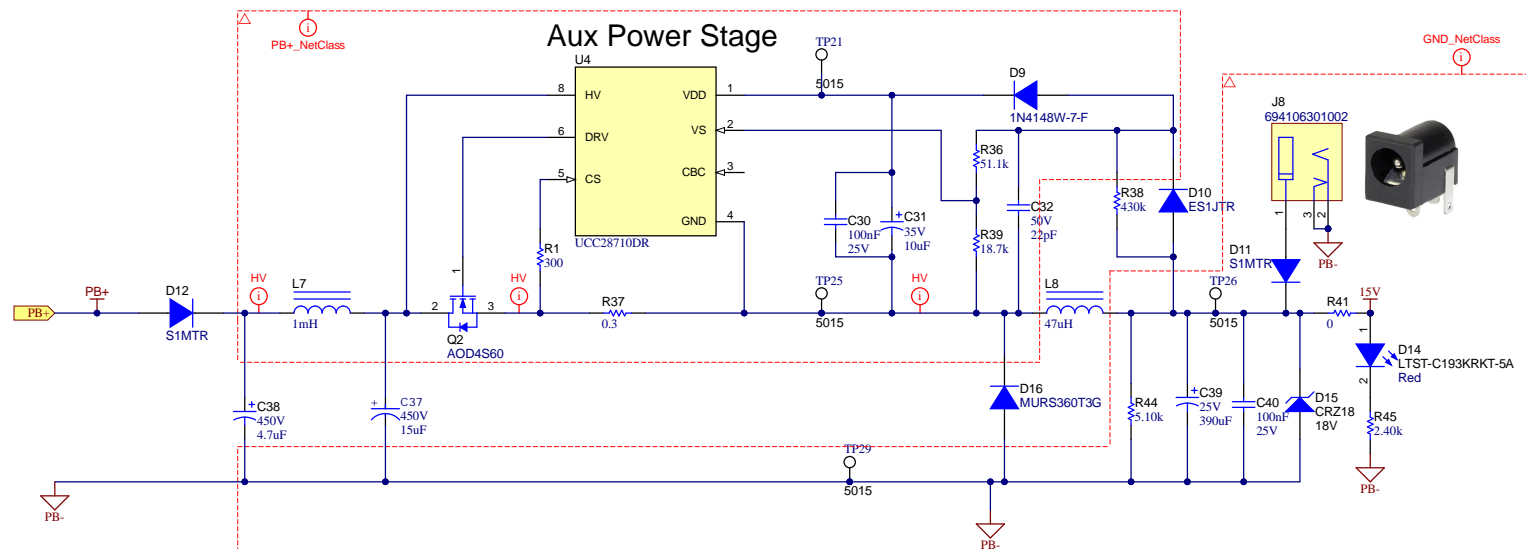
AC Input
220VAC/18A

Filter board

DC Output
400VDC/10A

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: Not Applicable	Designed for: Public Release	Mod. Date: 12/22/2022
TID #: TIDA-010236	Project Title: TIDA-010236 Main Board	
Number: TBD	Rev: B	Sheet Title: Main
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 2 of 6
Drawn By: Hely Zhang	File: Main.SchDoc	Size: A4
Engineer: Hely Zhang	Contact: http://www.ti.com/support	



CAUTION HOT SURFACE



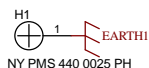
DANGER HIGH VOLTAGE



CAUTION HOT SURFACE



DANGER HIGH VOLTAGE



PCB
LOGO
Texas Inst

LBL1
 PCB Label
 THT-14-423-10
 Size: 0.65" x 0.20"

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo



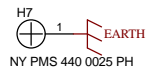
FID1



FID2



FID3



NY PMS 440 0025 PH

PCB Number: TBD
PCB Rev: B

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: Not Applicable	Designed for: Public Release	Mod. Date: 12/12/2022
TID #: TIDA-010236	Project Title: TIDA-010236 Main Board	
Number: TBD	Rev: B	Sheet Title: Aux
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 4 of 6
Drawn By: Hely Zhang	File: AuxP_SchDoc	Size: A3
Engineer: Hely Zhang	Contact: http://www.ti.com/support	

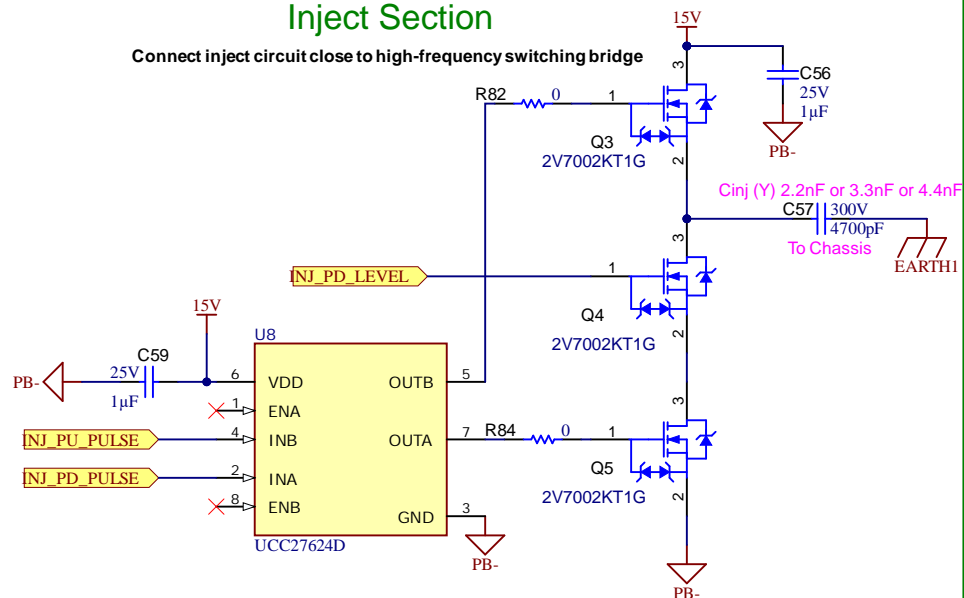


[illegible]

Orderable: Not Applicable	Designed for: Public Release	Mod. Date: 11/16/2022
TID #: TIDA-010236	Project Title: TIDA-010236 Main Board	
Number: TBD	Rev: B	Sheet Title: Control
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 5 of 6
Drawn By: Hely Zhang	File: ControlStage_SchDoc	Size: A3
Engineer: Hely Zhang	Contact: http://www.ti.com/support	

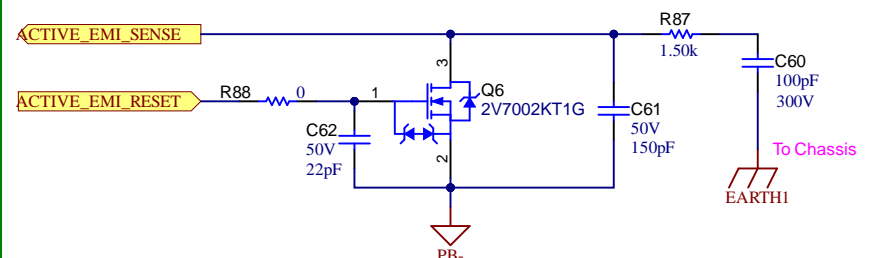
Inject Section

Connect inject circuit close to high-frequency switching bridge



Sense Section

Connect RESET circuit as close as possible to EMI_RESET and EMI_SEN pins
Minimize RESET FET gate-to-source/gnd loop



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: Not Applicable	Designed for: Public Release	Mod. Date: 3/21/2022
TID #: TIDA-010236	Project Title: TIDA-010236 Main Board	
Number: TBD	Rev: B	Sheet Title: Active EMI
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 6 of 6
Drawn By: Hely Zhang	File: ACTIVE_EMI.SchDoc	Size: A4
Engineer: Hely Zhang	Contact: http://www.ti.com/support	